UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 7,636,914 B1   Page 1 of 1
APPLICATION NO. : 10/692524
DATED   : December 22, 2009
INVENTOR(S)   : Steve Johnson

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 9, line 2 (claim 10, line 2) of the printed patent, please insert --VHDL,-- before VERILOG.

At column 9, line 2 (claim 10, line 2) of the printed patent, please delete "Verilog" before C#.

At column 10, line 34 (claim 18, line 3) of the printed patent, please insert --objects-- before to.

At column 10, line 49 (claim 23, line 2) of the printed patent, please change "Verilog" to --VERILOG--.

Signed and Sealed this

Twenty-third Day of March, 2010

David J. Kappos
Director of the United States Patent and Trademark Office